

GIACOMO DI LIBERTO

Brooklyn, NY

631-827-5961 ◊ giacomo.diliberto@law.nyls.edu

EDUCATION

New York Law School

Juris Doctor

GPA: 3.96

Honors: Dean's Scholar Award | Law Review Member | Dean's List Fall 2024, Spring 2025 & Fall 2025

New York, NY

Expected May 2027

Cornell University

Bachelor of Science, Electrical & Computer Engineering

GPA: 3.38

Honors: Dean's List Fall 2017 & Spring 2018

Relevant Coursework: Microcontrollers, Computer Architecture, Ubiquitous Computing, Computer Networks and Telecommunications, Digital VLSI Design Intelligent Physical Systems, Microelectronics, Embedded Systems, Signals and Information, Digital Logic and Computer Organization

Ithaca, NY

December 2018

PROFESSIONAL EXPERIENCE

Wilmer Cutler Pickering Hale and Dorr LLP

Incoming Summer Associate

· Working in the IP Litigation practice

New York, NY

Expected May 2026 - July 2026

Haley Guiliano LLP

Patent Agent

- Managed clients' patent portfolios including task docket along with related deadlines
- Drafted patent applications, including claims, figures, and detailed specifications
- Conducted invention disclosures interviews with engineers, scientists, and in-house counsel
- Provided patent strategies for domestic and foreign filings

New York, NY

September 2021 - September 2025

Marvell Technology

Verification Engineer

- Verified network interface subblocks for SOCs
- Created UVM testbenches to fully exert RTL designs of core's subblocks
- Designed code and functional coverage plans to ensure testing of stimuli corner cases
- Ensured low regression compile and run times through well-designed testbench methodology

Marlborough, MA

August 2019 - August 2021

CERTIFICATIONS/TECHNICAL SKILLS

USPTO Patent Practitioner

Reg. No. 81,758

Proficient Coding Languages

Verilog, SystemVerilog, C, Python

Familiar Coding Languages

Java, Matlab, C++, Assembly, Arduino, PyMTL

Languages

Italian (Conversational)

NOTABLE PROJECTS

Multi-Core Processor

Designed and tested processor, cache, memory and networks using Verilog

IoT Security System

WiFi-enabled doorway security with PIC32 microcontroller and ESP8266

Autonomous Maze Robot

Used Arduino and FPGA for robot and mapmaking basestation

Cube Game on FPGA

Designed and wrote code for an interactive cube avoidance game in C